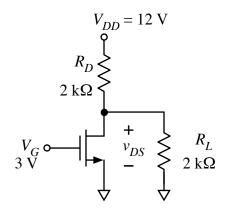
Find drain-source voltages for the transistors in the two circuits shown. The NMOS has $V_{Tn} = 1 \text{ V}$ and $K_n = 0.5 \text{ mA/V}^2$. For the PMOS, $V_{Tp} = -1 \text{ V}$ and $K_p = 0.222 \text{ mA/V}^2$.

NMOS: $v_{DS} =$



PMOS: *v*_{DS} = _____

$$V_{DD} = 12 \text{ V}$$

$$V_{G} \circ V_{DS} \Rightarrow R_{L}$$

$$+ \qquad 2 \text{ k}\Omega$$

$$R_{D} \circ V_{DS} \Rightarrow R_{L}$$